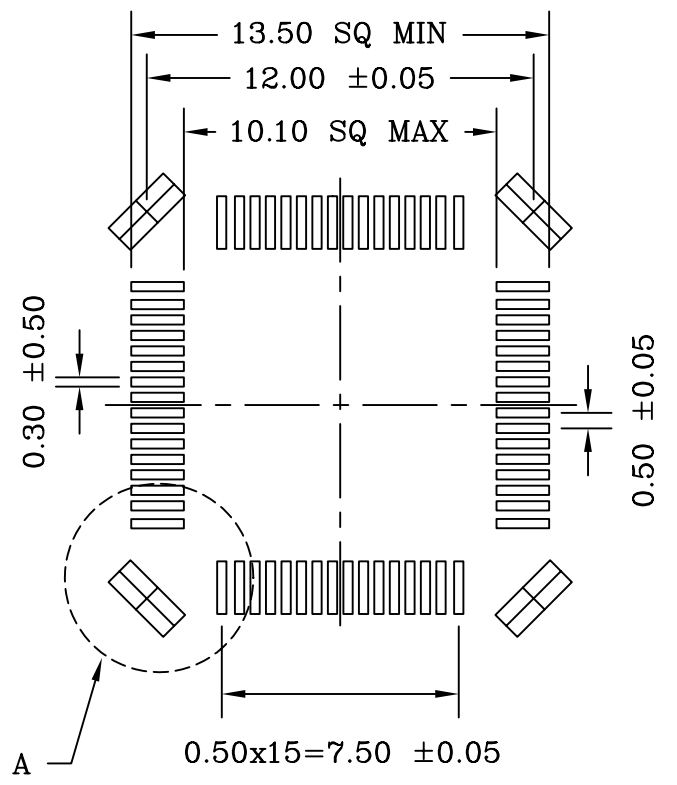
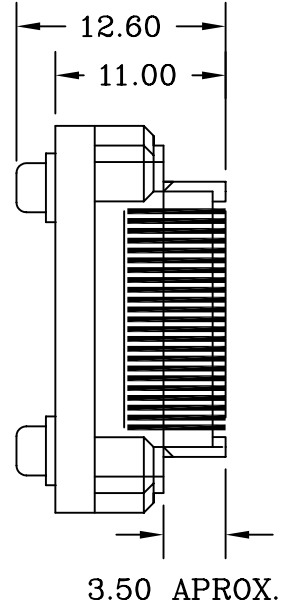
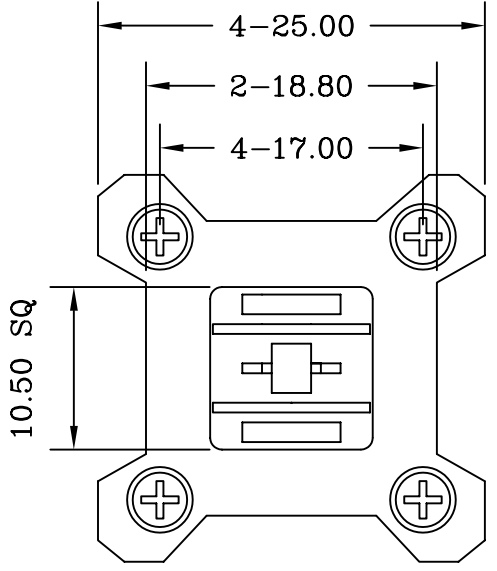


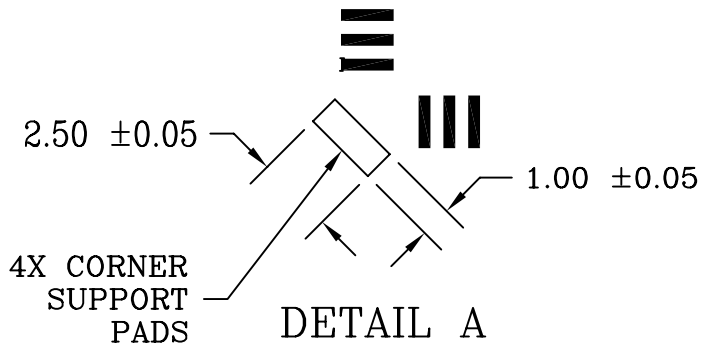
REV	DESCRIPTION	DATE	BY
A	NEW DRAWING	10/10/02	H.N.
B	ADDED NOTE GOLD PLATING	11/02/05	H.N.
C	REMOVED 2X ϕ .039 ALIGNMENT PINS	11/13/06	H.N.



SEE DETAIL A

RECOMMENDED PCB LAYOUT WITH OUT ALIGNMENT PINS

GOLD PLATING RoHS COMPLIANT

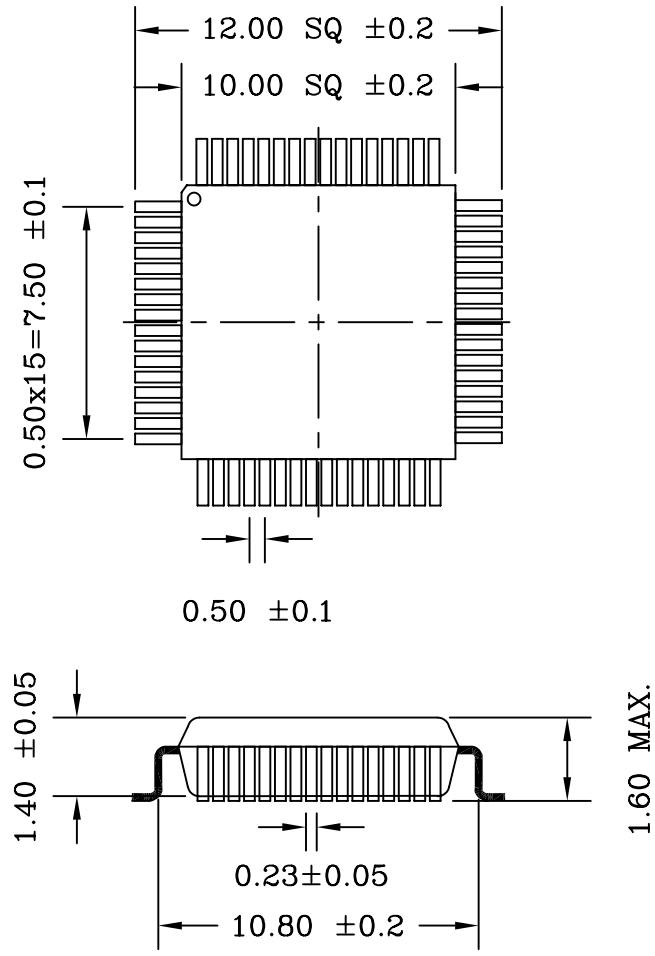


ALL DIMENSIONS ARE IN MILLIMETERS UNLESS OTHERWISE SPECIFIED

Emulation Technology, Inc.
— VLSI and SMT ADAPTERS and ACCESSORIES —

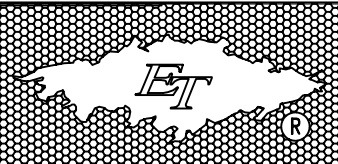
2344 Walsh Avenue, Bldg.F Santa Clara, Ca 95051 TEL:(408)982-0660 FAX:(408)982-0664

UNLESS OTHERWISE SPECIFIED: GENERAL TOLERANCES: \pm 0.3	SHEET: 1 OF 2	DATE: 10/10/02	REVISION: C	ASSEMBLY DRAWING
	CHECKED:	DRAWN: H.N.		ITEM: S-QFP-SM-064-D
	Scale N/A	DO NOT SCALE DRAWING		DESCRIPTION: S-QFP-SM-064-D



PACKAGE SPECIFICATIONS	
PIN COUNT	= 64
LEAD PITCH	= 0.50mm
PACKAGE SIZE	= 10.00mm
TIP TO TIP	= 12.00mm

ALL DIMENSIONS ARE IN MILLIMETERS UNLESS OTHERWISE SPECIFIED

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		2344 Walsh Avenue, Bldg.F Santa Clara, Ca 95051	TEL:(408)982-0660 FAX:(408)982-0664
SHEET: 2 OF 2	DATE: 10/10/02	REVISION: C	ASSEMBLY DRAWING
CHECKED:		DRAWN: H.N.	ITEM: S-QFP-SM-064-D
Scale N/A		DO NOT SCALE DRAWING	DESCRIPTION: S-QFP-SM-064-D