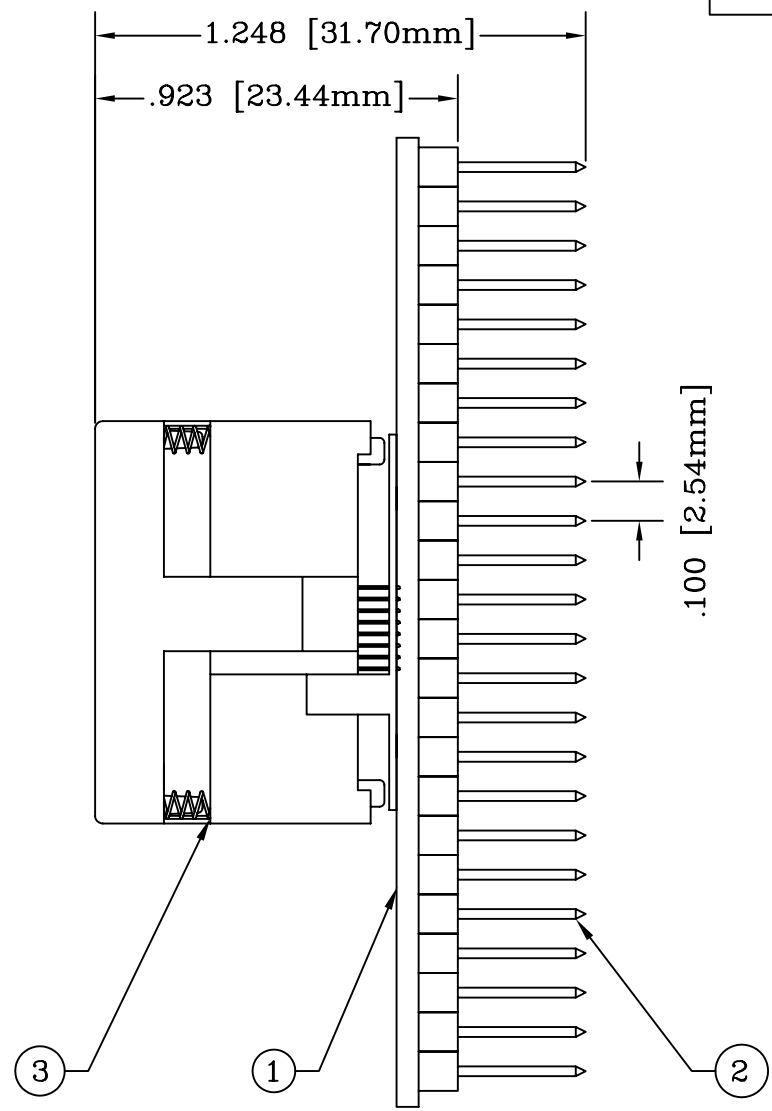
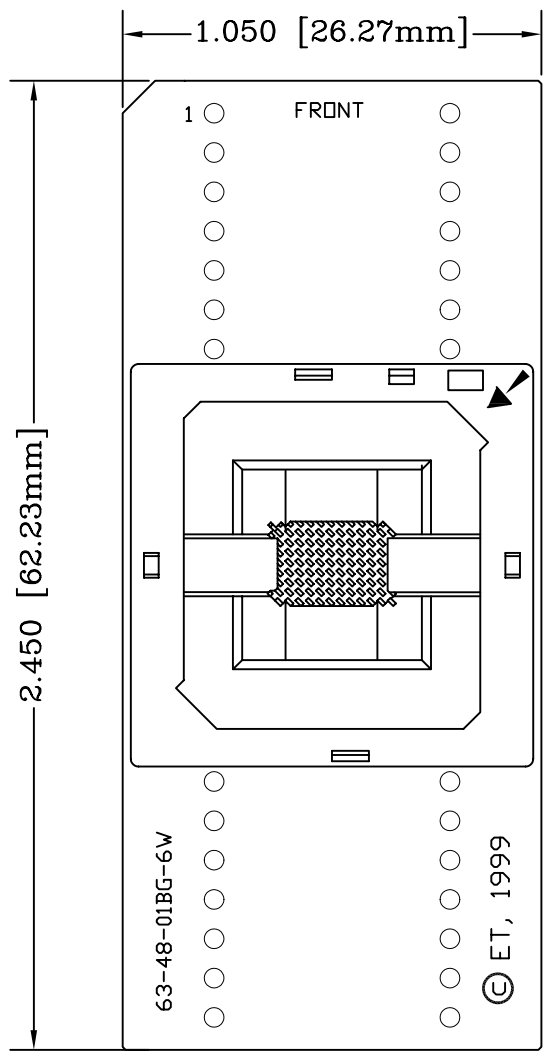
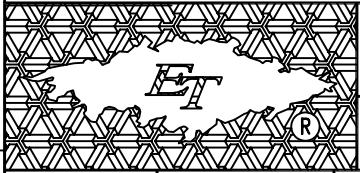


REV	DESCRIPTION	DATE	BY
A	NEW DRAWING	05/29/07	H.N.

F7985



ALL DIMENSIONS ARE IN INCHES UNLESS OTHERWISE SPECIFIED



**Emulation Technology, Inc.**  
 — VLSI and SMT ADAPTERS and ACCESSORIES —  
 2344 Walsh Avenue, Bldg.F Santa Clara, Ca 95051  
 TEL:(408)982-0660 FAX:(408)982-0664

NO	ITEM	REV	DESCRIPTION	QTY
1	63-48-01BG-6W	A	BASEBOARD	1
2	WWPOST-36-AS-II	A	SOLDER BOTTOM #1	1
3	S-MBG-12X8-63-B	A	SOLDER TOP #1	1

SHEET: 1 OF 2	DATE: 05/29/07	REVISION: A	<b>ASSEMBLY DRAWING</b>
CHECKED:	DRAWN: H.N.	ITEM: 6348026BG600GNG	
Scale N/A	DO NOT SCALE DRAWING	DESCRIPTION: AS-63-48-02BG-6WEL-GANG	

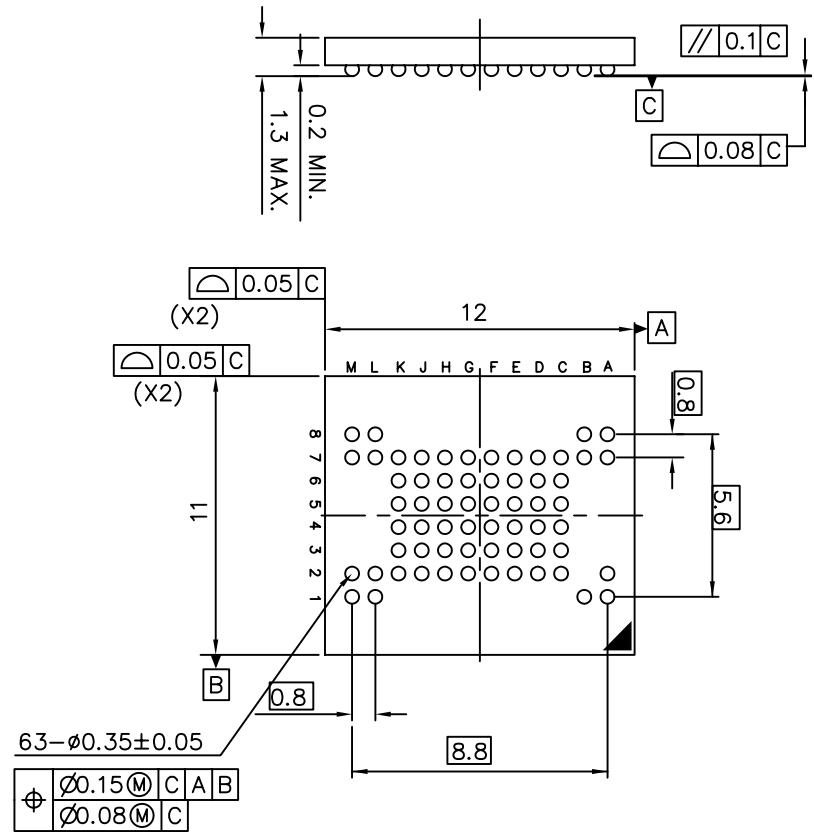
REV	DESCRIPTION	DATE	BY
A	NEW DRAWING	05/29/07	H.N.

F7985

**CONNECTION TABLE:**

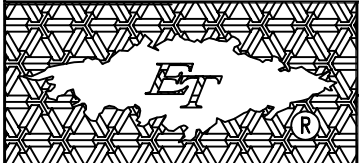
63-Pin FBGA SOCKET	PIN NAMES		48-Pin DIP BASE
	29DL322C/29LV033C		
K3	DAT1	DAT1	1
K4	DAT3	ADR21	2
J2	OE#	OE#	3
J3	DAT9	NC	4
H2	CE#	CE#	5
H3	DAT8	NC	6
H4	DAT10	DAT3	7
G2	ADR0	ADR0	8
G3	DAT0	DAT0	9
G4	DAT2	DAT2	10
G5	DAT5	DAT5	11
G6	DAT7	ADR19	12
G7	ADR16	ADR17	13
K2	VSS	VSS	14
H5	DAT12	NC	15
H6	DAT14	ADR10	16
J4	DAT11	VCC	17
H7	BYTE#	NC	18
J5	VCC	VCC	19
J6	DAT13	DAT6	20
J7	DA15/A-1	ADR20	21
K5	DAT4	DAT4	22
K6	DAT6	DAT7	23
K7	VSS	VSS	24
C7	ADR13	ADR14	25
C6	ADR9	ADR9	26
C5	WE#	WE#	27
D7	ADR12	ADR13	28
D6	ADR8	ADR8	29
D5	RESET#	RESET#	30
E7	ADR14	ADR15	31
E6	ADR10	ADR11	32
E5	NC	NC	33
F7	ADR15	ADR16	34
F6	ADR11	ADR12	35
F5	ADR19	NC	36
F4	ADR20	NC	37
F3	ADR5	ADR5	38
F2	ADR1	ADR1	39
E4	ADR18	NC	40
E3	ADR6	ADR6	41
E2	ADR2	ADR2	42
D4	WP#/ACC	ACC	43
D3	ADR17	ADR18	44
D2	ADR4	ADR4	45
C4	RY/BY#	RY/BY#	46
C3	ADR7	ADR7	47
C2	ADR3	ADR3	48

NC= No Connection



RECOMMENDED IC (REF)

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SHEET: 2 OF 2	DATE: 05/29/07	REVISION: A	<b>ASSEMBLY DRAWING</b>
CHECKED:	DRAWN: H.N.	ITEM: 6348026BG600GNG	DESCRIPTION: AS-63-48-02BG-6WEL-GANG
Scale N/A	DO NOT SCALE DRAWING		