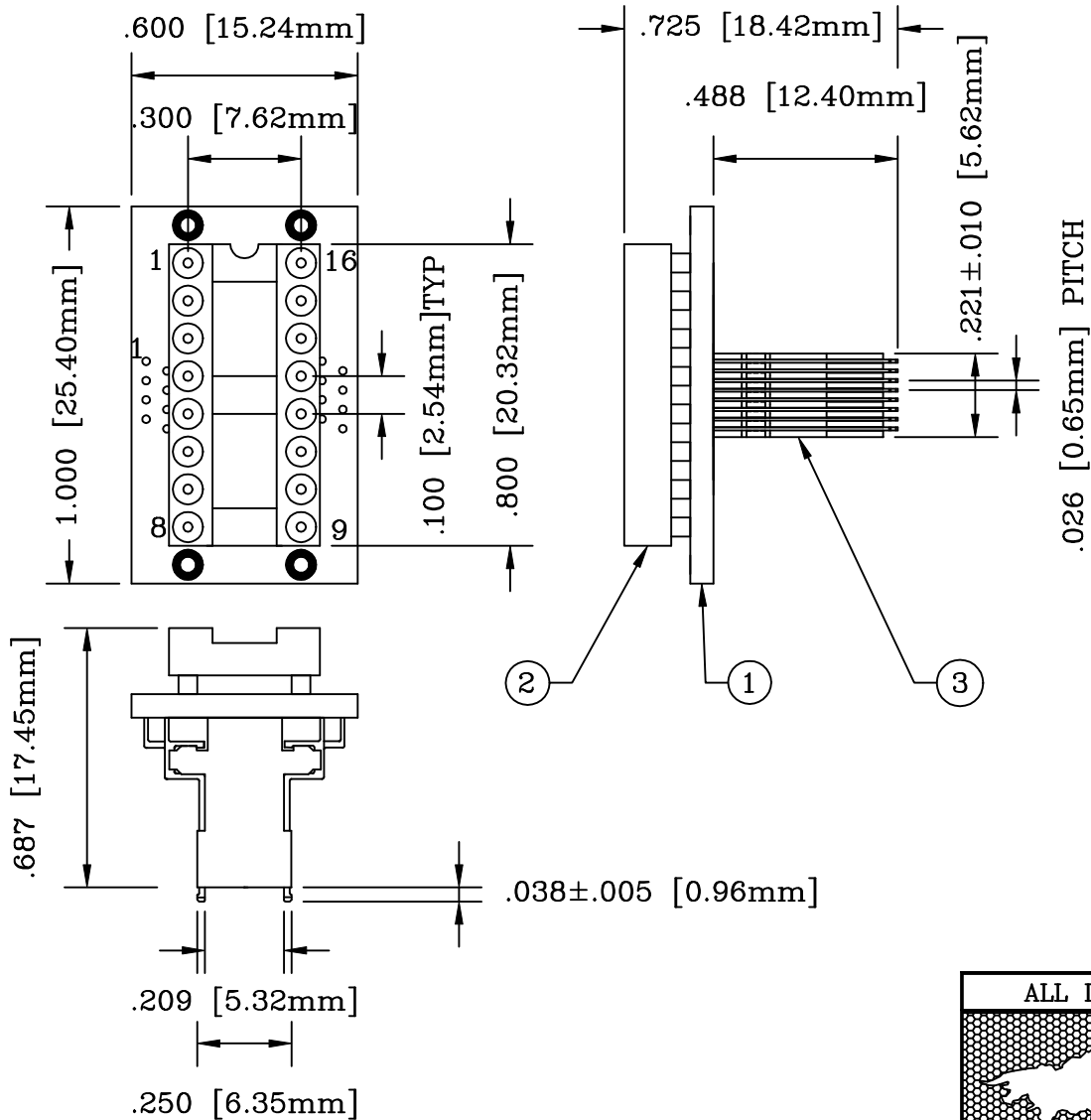


REV	DESCRIPTION	DATE	BY
A	NEW DRAWING	05-10-02	M.D.
B	ADDED PCB LAYOUT	06/12/02	H.N.
C	ADDED PINOUT	08/01/02	H.N.
D	INCLUDED TOLERANCES	01-30-04	M.D.

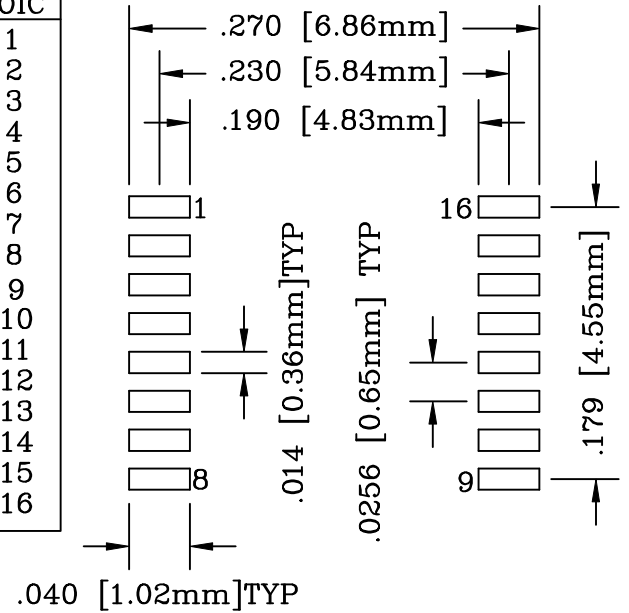
F6651  
DOD 36651



WIRED 1-1

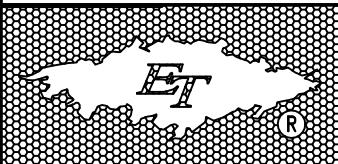
DIP	SOIC
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8
9	9
10	10
11	11
12	12
13	13
14	14
15	15
16	16

RECOMMENDED PCB LAYOUT



NO.	ITEM	DESCRIPTION	QTY.
1	20-DIP.3-ST03-S	BASE BOARD	1
2	S-DIP-00-016-A	MOUNT ON TOP OF #1	1
3	ST03-SPACER-16	MOUNT ON REAR OF #1	1

ALL DIMENSIONS ARE IN INCHES UNLESS OTHERWISE SPECIFIED



**Emulation Technology, Inc.**  
— VLSI and SMT ADAPTERS and ACCESSORIES —

2344 Walsh Avenue, Bldg.F Santa Clara, Ca 95051  
TEL:(408)982-0660 FAX:(408)982-0664

SHEET: 1 OF 1 DATE: 08/01/02 REVISION: C  
CHECKED: Perry Munroe DRAWN: Huy Nguyen  
Scale 1:2 DO NOT SCALE DRAWING

**ASSEMBLY DRAWING**  
ITEM: ASDIP.3016ST031  
DESCRIPTION: AS-DIP.3-016-ST03-1