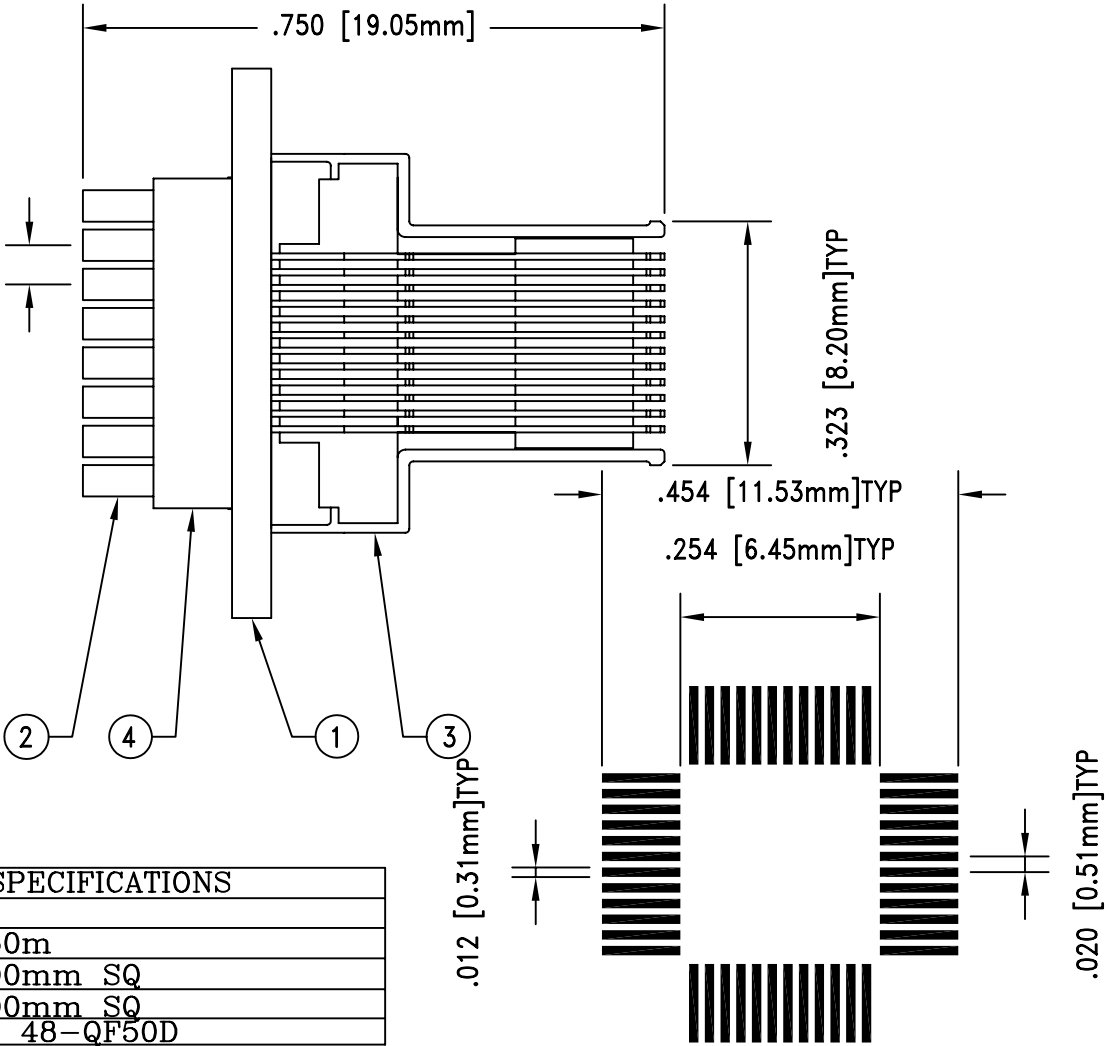
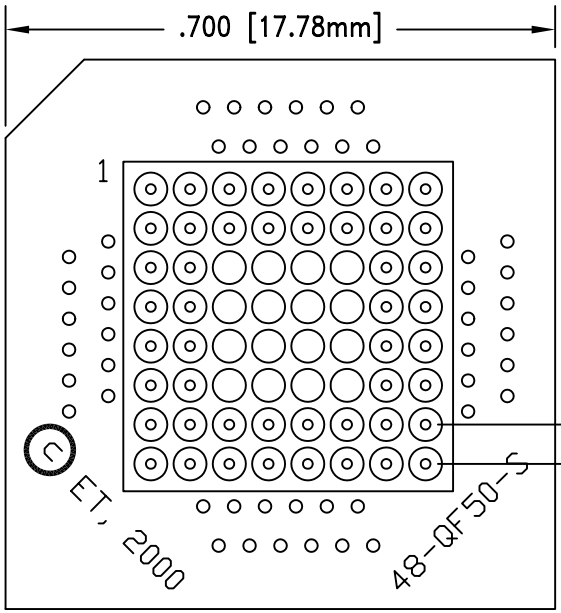


REV	DESCRIPTION	DATE	BY
A	NEW DRAWING	1/3/01	A.F.
B	ADDED PACKAGE SPECIFICATIONS	11/09/06	H.N.

F5807



PIN MAP

1	48	47	45	43	41	39	37
3	2	46	44	42	40	38	36
5	4					34	35
7	6					32	33
9	8					30	31
11	10					28	29
12	14	16	18	20	22	26	27
13	15	17	19	21	23	24	25

PACKAGE SPECIFICATIONS	
PIN COUNT	= 48
LEAD PITCH	= 0.50mm
PACKAGE SIZE	= 7.00mm SQ
TIP TO TIP	= 9.00mm SQ
ET PACKAGE CODE	= 48-QF50D

RECOMMENDED SMT LAYOUT

ALL DIMENSIONS ARE IN INCHES UNLESS OTHERWISE SPECIFIED

Emulation Technology, Inc.
 — VLSI and SMT ADAPTERS and ACCESSORIES —
 2344 Walsh Avenue, Bldg. F Santa Clara, Ca 95051
 TEL:(408)982-0660 FAX:(408)982-0664

SCALE 4:1

NO.	ITEM	DESCRIPTION	QTY.
1	48-QF50-S	BASE BOARD	1
2	PINS2	MOUNT ON TOP OF #1	48
3	QF50-SPACER-D	MOUNT ON REAR OF #1	1
4	8X8-PIN-HOLDER	MNT TOP # 1 WITH #2	1

SHEET: 1 OF 1	DATE: 1/30/01	REVISION: B	ASSEMBLY DRAWING
CHECKED: Perry Munroe	DRAWN: AARON FINE	ITEM: EPP048QF50DSM	
DO NOT SCALE DRAWING			DESCRIPTION: EPP-048-QF50D-SM