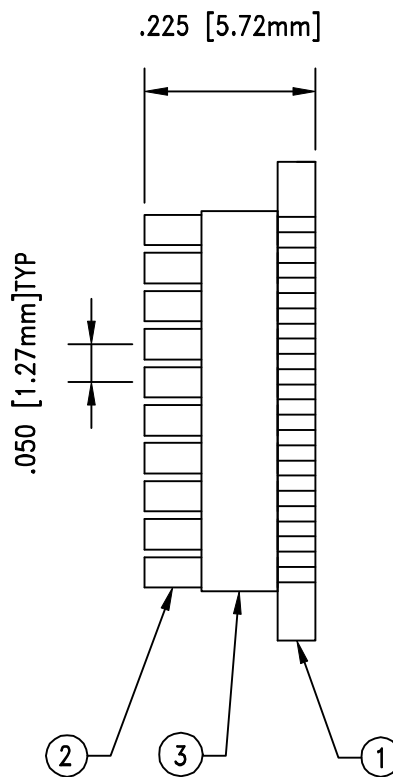
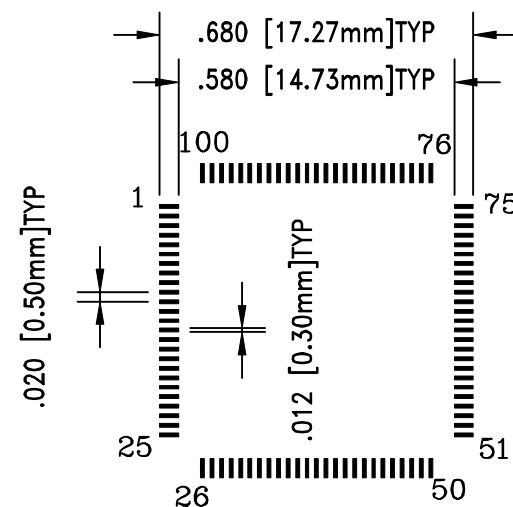


PIN MAP

1	100	98	95	92	90	85	81	78	76
3	2	99	96	91	89	84	80	77	75
6	5	4	97	93	88	83	79	74	73
10	9	8	7	94	87	82	72	71	70
15	14	13	12	11	86	69	68	66	67
17	16	18	19	36	61	62	63	64	65
20	21	22	32	37	44	57	58	59	60
23	24	29	33	38	43	47	54	55	56
25	27	30	34	39	41	46	49	52	53
26	28	31	35	40	42	45	48	50	51



SCALE 4:1



MINIMUM PAD LAYOUT

ALL DIMENSIONS ARE IN INCHES UNLESS OTHERWISE SPECIFIED



Emulation Technology, Inc.

— VLSI and SMT ADAPTERS and ACCESSORIES —

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NO.	ITEM	DESCRIPTION	QTY.
1	100-QF49D-LP	BASE BOARD	1
2	PINS2	MOUNT ON TOP OF #1	100
3	10X10-PIN-HLDR	PLUG ON TOP OF #1 WITH #2	1

SHEET: 1 OF 1	DATE: 11/6/00	REVISION:	ASSEMBLY DRAWING
CHECKED: Paula Olson	DRAWN: Aaron Fine	ITEM: EPP100QF49DSMLP	
DO NOT SCALE DRAWING			DESCRIPTION: EPP-100-QF49D-SM-LP