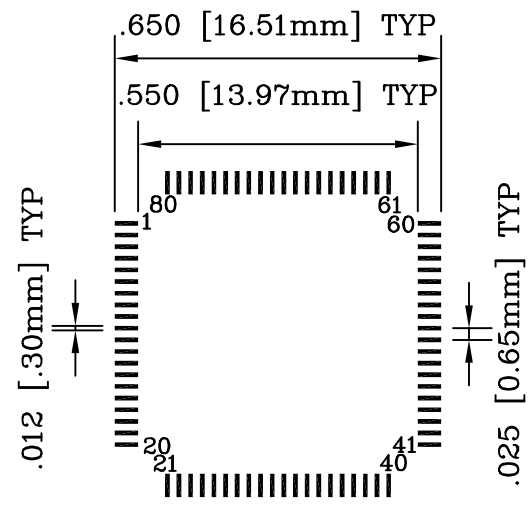
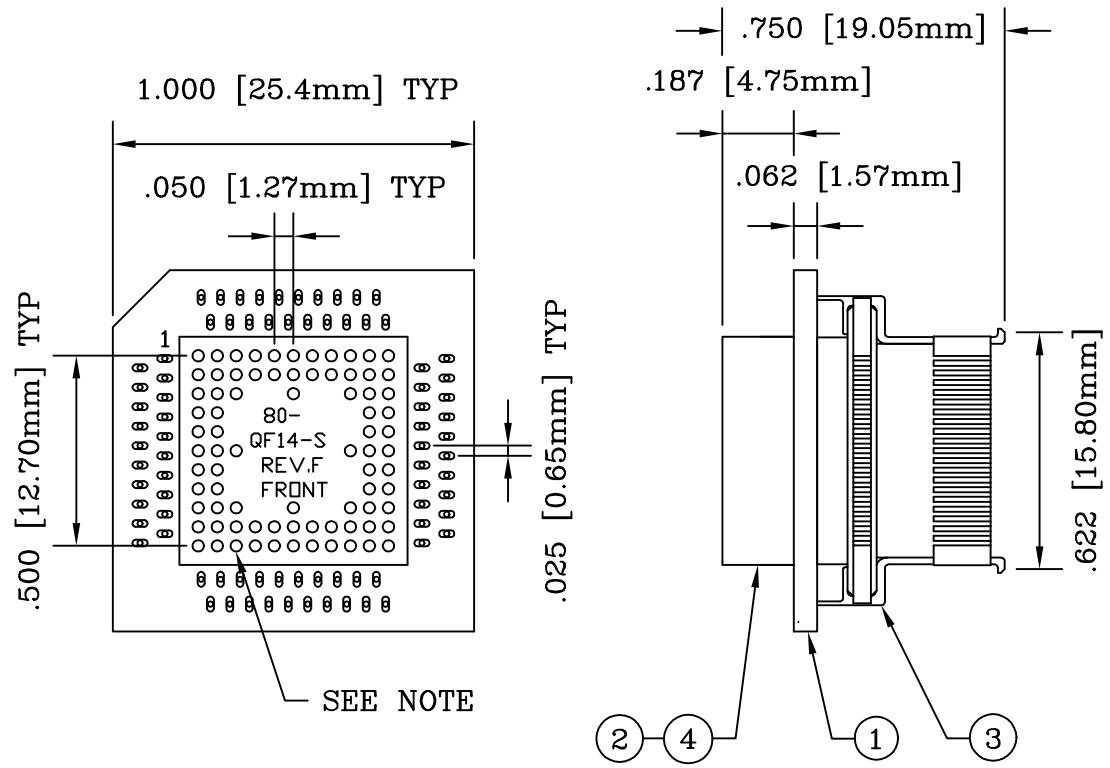


REV	DESCRIPTION	DATE	BY
A	NEW DRAWING	10/30/00	A.F.
B	UPDATED DWG & ADDED NOTE	12/13/02	H.N.

F3886
DOD 33886

PIN MAP

1	80	78	76	74	72	69	67	65	63	61
3	2	79	77	75	73	70	68	66	62	60
5	6	4		71		64	59	58		
7	8							57	56	
9	10							55	54	
12	13	11						51	53	52
14	15							50	49	
16	17							48	47	
18	19	24		31		44	46	45		
20	22	26	28	30	33	35	37	39	42	43
21	23	25	27	29	32	34	36	38	40	41



MINIMUM RECOMMENDED PAD LAYOUT

NOTE:
FEMALE PIN: ACCEPT PIN FROM ϕ .015 [0.38mm]
TO ϕ .022 [0.56mm]

ALL DIMENSIONS ARE IN INCHES UNLESS OTHERWISE SPECIFIED

Emulation Technology, Inc.
— VLSI and SMT ADAPTERS and ACCESSORIES —

2344 Walsh Avenue, Bldg.F Santa Clara, Ca 95051
TEL:(408)982-0660 FAX:(408)982-0664

SHEET: 1 OF 1 DATE: 12/13/02 REVISION: B

CHECKED: Perry Munroe DRAWN: Hanh Nguyen

Scale 2:1 DO NOT SCALE DRAWING

ASSEMBLY DRAWING

ITEM: EPP080QF14DSM
DESCRIPTION: EPP-080-QF14D-SM

NO.	ITEM	DESCRIPTION	QTY.
1	80-QF14-S	BASE BOARD	1
2	PINS2	MOUNT ON FRONT OF # 1	80
3	QF14-SPACER-A-D	MOUNT ON REAR OF # 1	1
4	SPACER-080Q14	MOUNT TOP WITH # 2	1