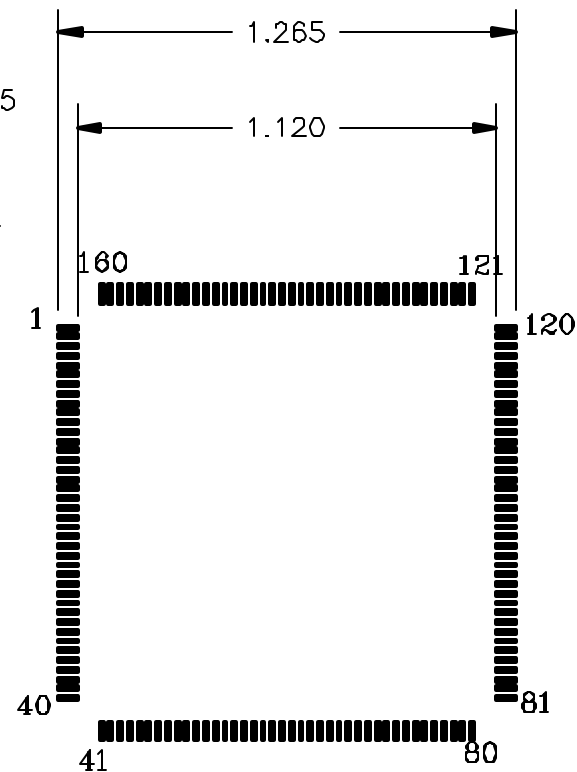


RECOMMENDED
MINIMUM PAD LAYOUT



160 159 157 156 155 154 153 152 151 150 149 148 147 146 145 144 143 142 141 140 139 138 137 136 135 134 133 132 131 130 129 128 127 126 125 124 123 122 121

1 2
3 4
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PIN MAP

120 119
118 117
116 115
114 113
112 111
110 109
108 107
106 105
104 103
102 101
100 99
98 97
96 95
94 93
92 91
90 89
88 87
86 85
84 83
82 81

41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80

ALL DIMENSIONS ARE IN INCHES UNLESS OTHERWISE SPECIFIED



Emulation Technology, Inc.

— VLSI and SMT ADAPTERS and ACCESSORIES —

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NO.	ITEM	DESCRIPTION	QTY.
1	MX-160-QF07-W	TOP SUB ASSEMBLY	1
2	EPP160QF07SM	BOTTOM SUB ASSEMBLY	1

SHEET: 1 OF 1	DATE: 4-6-94	REVISION:	ASSEMBLY DRAWING
CHECKED: CABOR KARDOS	DRAWN: Paula McKenna	ITEM: EPP160QF07W	
DO NOT SCALE DRAWING			DESCRIPTION: EPP-160-QF07-W