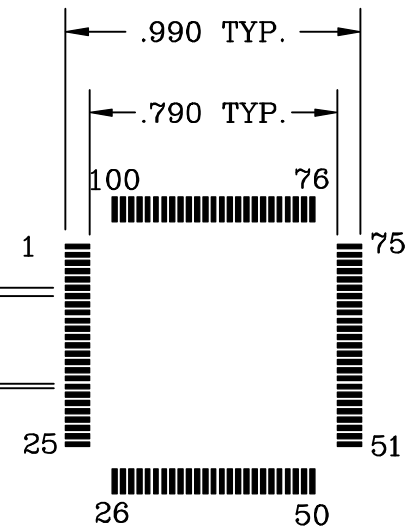


RECOMENDED
MINIMUM PAD LAOUT



100	99	97	95	93	91	89	87	85	83	81	79	77	76
1													
3			98	96					84	78			73
5				94						80			71
7	2				90	88	82						74
9	6	4				92	86				72	70	67
11			10	8					68	66			65
13				12	14				64	62			63
15				16	18				58	60			61
17	20	22				36	42				54	56	59
19	24					32	38	40					52
21													55
23				30							44		53
25					28	34					46	48	51
26	27	29	31	33	35	37	39	41	43	45	47	49	50

PIN MAP

	Emulation Technology, Inc.	
	— VLSI and SMT ADAPTERS and ACCESSORIES —	
	2344 Walsh Avenue, Bldg. F Santa Clara, Ca 95051	TEL:(408)982-0660 FAX:(408)982-0664

NO.	ITEM	DESCRIPTION	QTY.
1	MX-100-QF01-W	TOP SUB ASSEMBLY	1
2	EPP100QF01SM	EPP-100-QF01-SM	1

SHEET: 1 OF 1	DATE: 11-01-00	REVISION:	ASSEMBLY DRAWING
CHECKED: Paula Olson	DRAWN: Miguel Delgadillo	ITEM: EPP100QF01W	
DO NOT SCALE DRAWING			